Abstracts

A Ka-Band Planar Tripler Based on a Stacked Symmetric InP Heterostructure-Barrier Varactor

K. Krishnamurthi, E. Boch and R.G. Harrison. "A Ka-Band Planar Tripler Based on a Stacked Symmetric InP Heterostructure-Barrier Varactor." 1995 MTT-S International Microwave Symposium Digest 95.2 (1995 Vol. II [MWSYM]): 549-552.

The symmetric heterostructure barrier varactor (HBV) lends itself to frequency tripling with no need for bias or for a second harmonic idler circuit. This paper presents the performance of the first planar microstrip Ka-band frequency tripler based on a stacked HBV structure. A minimum conversion loss of 11.2 dB with a maximum power output of 10.78 dBm at 40.35 GHz is obtained.

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